Research Report

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Yukio Watanabe and Motochika Okano

Kyushu Institute of Technology Department of Electrical Engineering Sensui 1-1, Tobata Kitakyushu Fukuoka 804-8550 Japan

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Surface conduction on a transparent BaTiO₃ crystal suggesting a possible surface electron layer

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Kyushu Institute of Technology, Department of Electrical Engineering, Sensui 1-1, Tobata, Kitakyushu, Fukuoka 804-8550, Japan

Abstract - The increasing demand to incorporate miniature ferroelectrics in large-scale integrated circuits (LSI) has triggered a renewed interest in their size effect¹⁻⁸. The surface of the ferroelectric, which influences the size effect significantly, is unique because that is where the macroscopic electric charges caused by the ferroelectric spontaneous polarization appear. There has been speculation that such a field may form a special layer on the free ferroelectric surface⁹⁻¹². If such a layer exists, it will change our understanding of the fundamental properties and of the limitations of a very small ferroelectric, because the layer can be regarded as a natural electrode. However, its direct observation has not been reported so far, and the surface electrons have been usually regarded as immobile, if they exist at all. Here, we report conduction measurements on the clean, free surface of insulating single crystals of a prototype ferroelectric system, BaTiO₃, in high vacuum. We find that the BaTiO₃ crystals exhibit surface conductance that is dependent on the spontaneous polarization and shows semimetallic temperature dependence. The observations provide evidence of a two-dimensional electron or hole layer on a clean, free ferroelectric surface that may be regarded as a ferroelectric metal.

1. Introduction

Typical ferroelectric perovskite oxides such as BaTiO₃ and PbTiO₃ are usually regarded as insulators, one of whose most basic property is a spontaneous polarization P_S that is reversible by an external electric field. The macroscopic electric charges due to P_S appear only at the surface, the interface, or the domain boundary. Therefore, various properties have been explained in a way similar to that of magnetism by comparing P_S with the magnetization M_S . For example, the 180° domain configuration (Fig. 1a), is usually explained by a Kittel model for the 180° magnetic domains¹³. When the surface of the ferroelectric is electrodeless, it is conventionally thought that an enormous depolarization field appears and is minimized by forming a multidomain structure. In a very thin ferroelectric, the ferroelectric phase is expected to exist only in a certain range of domain width, which poses severe constraints on the switching of P_S , or destabilizes the ferroelectric phase in an extreme case, limiting the minimum size of the ferroelectric phase.

These textbook concepts may be changed, especially in miniature ferroelectrics, if we consider the electrons originating from the finite energy band gap of the ferroelectrics because the depolarization field can be screened by these electrons. As seen in Figs. 2a and b, the situation of the free ferroelectric surface is similar to the SiO₂/Si interface in the metal/oxide/Si (MOS) diode under the electric field, suggesting a possible carrier generation at the ferroelectric surface by the depolarization field. Indeed, several observations are puzzling if no mobile charges exist at the ferroelectric surface. For example, the natural formation of head-on domains¹⁴ (Fig. 1b), a successful explanation of the 90° domain configuration (Fig. 1c) by stress and strain without electrostatis $tics^{5,6}$, and the switching and the phase stability of a ferroelectric in metal/ferroelectric/insulator/semiconductor structures⁸ (Fig. 1d) contradict the expectations based on the conventional electrostatics of ferroelectrics¹⁵. On the other hand, theories considering a finite band gap of the ferroelectric explain these observations and predict a carrier layer on the ferroelectric surface^{12,16,17}. Indeed, the electric field induces carriers in a superconducting perovskite oxide¹⁸. Furthermore, the $P_{\rm S}$ -induced mobile carriers persist for more than 10 months on the surface of a perovskite oxide, having a band gap of 2 eV^{19} . These observations suggest a possibility of the carrier generation in ferroelectric perovskites, although they possess a band gap of 3 - 4 eV, which requires larger band bending and may induce other screening mechanisms. Namely, a twodimensional electron gas may exist near the free surface of the ferroelectric and improve significantly the size limit of the ferroelectric phase without electrodes. However, the physical nature of a clean ferroelectric surface remains unclear²⁰⁻²³, and the direct experimental study of such a layer has been missing so far. To identify the existence and the basic nature of the hypothetical electron layer, we have investigated the current transport on the clean, free surface of BaTiO₃.

Surface conduction that is not due to adsorption and defects is measurable only on a clean surface of a high-quality single crystal in a high vacuum or at the domain boundaries¹². Asgrown single crystals of BaTiO₃ grown by a flux method, which were lightly yellowish, transparent, and insulating, were used to avoid polishing procedures and possible surface damage. We have developed a technique that allows the control of P_S at the electrodeless surface of a ferroelectric. This is achieved by a pair of *T*-shaped Pt electrodes sputtered on the top surface of the crystals and one on the bottom surface (Fig. 2c). To avoid contamination and damage, the electrodes were defined by masks instead of a lithography process, thus avoiding mechanical contact of the gap area during the process. We confirmed the absence of the sputtered materials in the gap area. The results below are for a crystal shown in Fig. 2c with a shiny smooth surface. To control $P_{\rm S}$, we build first a randomly oriented state by a temperature cycling across the Curie temperature $T_{\rm C}$ of 130 °C, and identify it by a polarized-light optical microscope. Then, we apply a dc electric field, e.g., 800 V/cm, between the top two electrodes and the bottom electrode slightly above $T_{\rm C}$, and allow it to cool. The resulting state is optically isotropic in-plane below $T_{\rm C}$, indicating that the optical axis and, therefore, $P_{\rm S}$ are perpendicular to the surface. The validity of the procedure is further confirmed by measuring a pyroelectric current on a small electrode to which the same procedure is applied. No change or deposition was detected on the surface of the gap area by an optical microscope after many poling procedures. Unless stated otherwise, all conductance data below were measured at a pressure of 3×10^{-8} torr in a vacuum chamber in darkness. The results below are obtained after several temperature cycles between room temperature and 150 °C in vacuum.

The surface conductance is measured each time after orienting the polarization of the gap area randomly and then poling the area either positive (negative bias on the top surface) or negative (positive bias on the top surface). The measurements are performed at 70 °C shortly after the poling procedure to obtain the conductance of a fresh surface. The current-voltage (I-V) characteristics of the positively poled state exhibit an ohmic relation and a 10 times higher conductance than a negatively poled and a randomly oriented state (Fig. 3). The current-time (I-t) characteristics at a fixed voltage indicate that the current is not transient due to the trap emission or the dielectric response. The polarization dependence is extremely small and irreproducible when the surface conductance is measured in air. Furthermore, the surface conductance in air is usually lower than that of all the polarization states in vacuum. In addition, the conductance is much lower than the above results when the poling is performed in air and measured in vacuum. The surface conduction of the positively poled state decreased after 12 days in vacuum but remained higher than that of the other states. All these observations indicate that the conduction takes place on the surface and is controlled by the polarization. Furthermore, we did not observe these phenomena in pure and reduced SrTiO₃ single crystals that are paraelectric. In addition, we have confirmed that these observations were not due to the change of the Schottky contacts by the current.

The surface conductance shows activation-type conduction from 200 °C to a temperature below room temperature. Figure 4a is an example of a temperature dependence of the positively poled state in which only approximately 50% of the gap area can possibly be poled due to the increase of the coercive field. The temperature dependence between 0 and -100 °C is similar to that of bulk conduction of heavily doped or semimetallic BaTiO₃. The spike in the curve is due to the pyroelectric current and the trap emission. The surface conduction at -170 °C shows ohmic *I-V* characteristics and does not decrease with time (Fig. 4b). A marked observation is the increase of the surface conduction and the hopping conduction between the trap levels as the origin of the surface conduction. On the other hand, the conduction between the top and bottom electrodes, which is regarded as the conduction through the bulk, shows only the current due to the dielectric response, which diminishes with time (Fig. 4b). The temperature dependence of the negatively poled state, which is almost 100% expressed in terms of an area ratio, is more remarkable (Fig. 4c). The conductance at -170 °C is higher than that at room temperature.

The preceding results are consistent with the picture that an electron layer is formed at the free ferroelectric surface. Theoretically, the electron layer can also form at the domain boundaries such as the head-on or the 90° domain boundaries if the charges are not completely screened by ions at these boundaries (Fig. 1c). Indeed, one of the randomly polarized states formed during 30

poling cycles exhibited an appreciable conduction between the top and bottom electrodes that was comparable to the surface conduction and was observed at -170 °C. The observations suggest conduction by the percolation of the current path at the domain boundaries. In this view, the hysteresis and relaxation of the current in the randomly oriented state, which are larger than those in the poled state in Fig. 3, can be explained as the carrier emission from the domain boundary. However, it is worthwhile to reemphasize that in the poled states the conduction between the top and bottom electrodes is always lower than the surface conduction.

The above results support the existence of the surface conduction, which is probably due to a surface carrier layer formed by P_S . Such a surface carrier layer donates the freedom and the moving capability to the domains, especially 180° domains, that determine the macroscopic and the mesoscopic properties of the ferroelectric. For example, the ferroelectric field-effect transistors⁸ can have a switching capability, and the ferroelectric phase in these structures is stabilized (Fig. 1d). Furthermore, the relaxation of P_S near the surface (∇P) is considered to be an important mechanism of the destabilization of the small ferroelectrics, which can also be present on a electroded ferroelectric surface^{22,23}. The simple modeling of this effect is the tri-layer structure of the ferroelectric consisting of two thin insulating surface layers with $P_S = 0$ and the layer having a bulk value of P_S (Fig. 1e). Therefore, the surface electron layer can solve various limitations posed by surface relaxation.

A theoretical calculation predicts the existence of a two-dimensional carrier layer on a ferroelectric surface¹². The accumulation of electrons at the surface is expected in positively poled states, whereas a weak inversion of whole carriers is expected in negatively poled states. When the quantization of the motion perpendicular to the surface is considered²⁴, the surface electron layer with a thickness of a few nanometers is predicted to exist a few lattices constants below the surface²⁵. This surface may not necessarily be a physical surface but, in a more exact definition, a location where $P_{\rm S}$ effectively terminates. The maximum charge density in the absence of traps is estimated to be 10^{21} - 10^{22} cm⁻³, the total charge of which is approximately the same as P_S. Using the reported value of bulk mobility of $1 \text{ cm}^2/\text{Vs}$ and the experimental surface conductivity, the carrier density per area is estimated to be 10^8 cm⁻². For a surface layer with thickness of a few nanometers, the carrier density is 10^{15} cm⁻³. These numbers should be regarded as the minimum values, because the mobility is expected to be reduced significantly by the scattering at the surface and the coupling with $P_{\rm s}$. Moreover, in the present sample, the surface contains many traps and adsorbents and, therefore, most of the carriers are probably trapped. From this, we may conclude that a surface electron or hole layer exists that has an enormous surface carrier density which is close to $P_{\rm S}$, and that a fraction of these electrons or holes exists as free carriers. The conductance can be further increased, however, by improving the BaTiO₃ surface.

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Figure 1 Typical domain configurations. Cross sectional view of 180° stripe domains, **a**. Head-on domains, **b** and 90° (a/c) domains, **c**. The arrows indicate the direction of P_S, and the shaded areas indicate the locations where the carrier is possibly generated. The dark shaded area corresponds to the metal electrode. Carrier generation in a typical heterostructure used for field effect transistors, **d**. Modeling of a capacitor structure (metal/ferroelectric/metal) where P_S has surface relaxation and the carrier generation, **e**. The ferroelectric layer is modeled by an insulator (P_S = 0)/ferroelectric (bulk value P_S)/insulator (P_S = 0).



Figure 2 Sample and the comparison of the field effect in a metal/oxide/Si diode, where the field effect is due to spontaneous polarization in the ferroelectric. Inverted view of the conventional metal/oxide/Si (MOS) diode, a. The current from S to D is controlled by the voltage at the metal gate electrode, because SiO₂ is polarized as shown by positive and negative charges and induces the electrons (e) at the SiO₂/Si interface. Schematic diagram of the ferroelectric with one side covered with metal and the other side in contact with the vacuum b. The origin of the carrier accumulation is similar to that in MOS, and the charge distribution in c is similar to that in b, except that the carriers are now located in the ferroelectric. The difference is the location of the carrier that is in the ferroelectric. Schematic diagram of the sample, c. Two *T*-shaped metal electrodes are deposited on the top surface and one (shown by the dotted line) is deposited on the bottom surface of a BaTiO₃ single crystal. The gap area (shaded) is poled.



Figure 3 Polarization dependence of the surface current. Current-voltage characteristics, **a**. Time dependence of current at a fixed voltage of 1 V at 70 °C, **b**. Solid lines, dashed lines, and dotted lines correspond to a positively, a negatively, and a randomly polarized state, respectively. The dash-dotted lines in Fig. 3b correspond to the state that is kept 12 days in vacuum after positive poling.



Figure 4 Temperature dependence of the surface conduction and comparison with bulk conduction. Temperature dependence of current at a fixed voltage of 1 V with an enlarged view of a low-temperature region (inset), a. Approximately 50% of the gap area is positively poled. The big spikes near -100 and 0 °C are due to the pyroelectric current at the successive phase transitions. Other spikes are probably due to trap emission. Time dependence of the current at a fixed voltage of -1 V, b, of the same state as in Fig. 4a. The solid, dashed, dotted, and the dash-dotted lines correspond to the surface conduction at -170 °C, the bulk conduction at -170 °C, the bulk conduction at 30 °C, and the surface conduction at 20 °C, respectively. Time dependence of the surface current at a fixed voltage of -1 V of the negatively poled state, c. The solid, dashed, and dotted lines correspond to the surface conduction at -170 °C, before cooling, and at 20 °C after cooling, respectively.